

Prof. Krishna Saraswat is Rickey/Nielsen Professor in the School of Engineering, Professor of Electrical Engineering and by courtesy Professor of Materials Science & Engineering at Stanford University.

He was born in Pilani, Rajasthan, India and received all his initial education in Pilani leading to B.E. degree in Electronics in 1968 from the Birla Institute of Technology and Science (BITS). He moved to USA in 1968 and received M.S. and Ph.D. degrees in Electrical Engineering in 1969 and 1974 respectively from Stanford University. After graduating he joined Stanford University as a Research Associate in 1975 and later became a Professor of Electrical Engineering in 1983. He also has an honorary appointment of an Adjunct Professor at BITS, Pilani since January 2004 and a Visiting Professor during 2007 at IIT Bombay, India. He has been a technical advisor, board member and consultant to several industrial organizations in USA, Asia and Europe. He has also advised several academic and government organizations all over the world, including Indian Government, BITS Pilani, IISC Bangalore and IIT Bombay.

Professor Saraswat's research interests are in new and innovative materials, structures, and process technology of silicon, germanium and III-V devices and interconnects for VLSI and nanoelectronics. Areas of his current interest are: new device structures to continue scaling MOS transistors, DRAMs and non volatile memories to nanometer regime, 3-dimensional ICs with multiple layers of heterogeneous devices, metal and optical interconnections and high efficiency and low cost solar cells.

Prof. Saraswat has supervised more than 85 doctoral students, 30 post doctoral scholars and has authored or co-authored 15 patents and over 785 technical papers, of which 10 have received *Best Paper Award*. He is a Life Fellow of the IEEE. He received the Thomas Callinan Award from The Electrochemical Society in 2000 for his contributions to the dielectric science and technology, the 2004 IEEE Andrew Grove Award for seminal contributions to silicon process technology, Inventor Recognition Award from MARCO/FCRP in 2007, the Technovisionary Award from the India Semiconductor Association in 2007, BITS Pilani Distinguished Alumnus Awards in 2012 and the Semiconductor Industry Association (SIA) Researcher of the Year Award in 2012. He is listed by ISI as one of the Highly Cited Authors in his field.